

Course Description

This course expands on concepts introduced in the “FPGA Synthesis with Synplify Pro®Tool” training class. The focus will be on complex design techniques and high-performance design.

Through the design experience of Synplicity® Corporate Application Engineers, students will learn to use the more powerful features of the Synplify Pro product to achieve the highest performance design implementations in today’s high-capacity devices.

Prerequisite for this course is FPGA Synplify Pro Tool training class and familiarity with the Synplify Pro product.

Level – Advanced

Course Duration – 1 day

Price – \$600 USD

Course Part Number – SYN-ADV-FPGA-101606

Who Should Attend? – FPGA Designers who are new to the Synplify Pro product.

Prerequisites

- Familiarity with Synplify Pro product or have completed the FPGA Synthesis with Synplify Pro Tool training class.

Topics covered include:

- HDL Coding for Performance
- Applying Timing Constraints
- Gated Clock Conversion
- Retiming and Pipelining Techniques
- Technology-Independent Attributes
- Technology-Specific Attributes
- Architecture-Specific Topics
- MultiPoint™ Synthesis Flow

Class Instruction

Class instruction includes lecture, lab and handouts of class materials. All classes are taught at our corporate headquarters in Sunnyvale, California by our Corporate Application Engineers who are experts with Synplicity products. Classes can also be arranged at your facility.

Register Today

TSI delivers public and private courses in locations throughout the world. Please contact us for more information, to view schedules, or to register online.

Visit www.technically-speaking.com for more company information,

Call **(702) 736-4116** for registration data or email us at eric@technically-speaking.com

Xilinx training credits are not honored at this time.

You must have your tuition payment information available when you enroll. We accept credit cards (Visa, MasterCard, or American Express) as well as purchase orders and training credits.

Practical HDL is a comprehensive VHDL/Verilog self-paced multimedia training environment.

This tool both reinforces topics covered during the class and offers additional advanced subject matter.

Cost - \$195 USD

